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THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: Hiroshi HASHIMOTO et al.

Art Unit: 2814

Application Number: 10/083,533

Examiner: Thao X. Le

Filed: February 27, 2002

Confirmation Number: 6400

SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE INCLUDING A For: SEMICONDUCTOR DEVICE HAVING A STABLE THRESHOLD CHARACTERISTIC

Attorney Docket Number:

020244

Customer Number:

38834

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. $\S1.97(c)(1)/(e)(1)$

Commissioner for Patents P. O. Box 1450 Alexandria, VA 22313-1450 June 18, 2008

Sir:

Applicants direct the attention of the Patent and Trademark Office to the documents listed on the attached Form PTO/SB/08. A copy of each non - U.S. document is attached.

This Information Disclosure Statement (IDS) is being submitted after issuance of a first official action on the merits and expiration of the three month period following the filing date or the entry of the national stage for the above-captioned application, but prior to issuance of either a final official action or a Notice of Allowance.

The undersigned hereby certifies that each item of information contained in the IDS was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the IDS. A copy of the foreign communication citing the documents is attached.

Application No.: 10/083,533 Information Disclosure Statement under 1.97(c)(1)

Art Unit: 2814 Attorney Docket No.: 020244

If there are any fees due in connection with the filing of this paper, please charge Deposit Account No. 50-2866.

Respectfully submitted,

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Enclosures: PTO/SB/08

(2) Documents